FIG.1

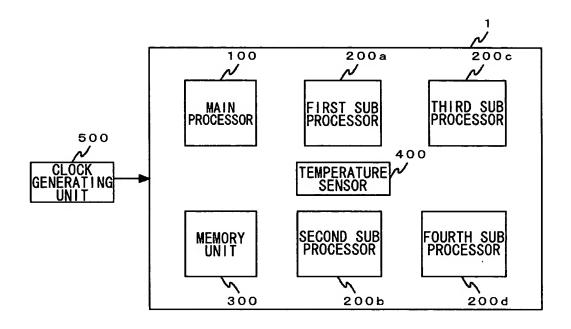


FIG.2

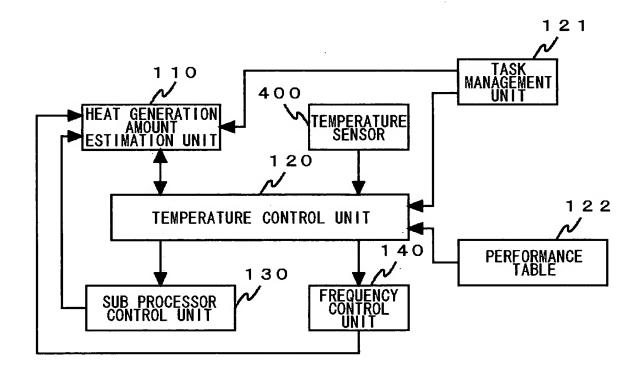


FIG.3

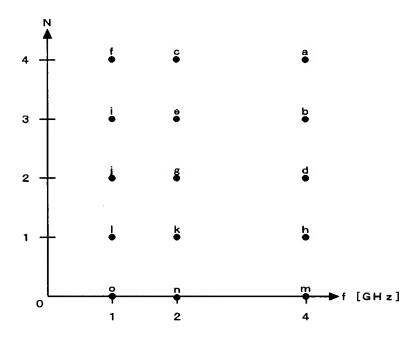


FIG.4

	NUME	BER OF USED	SUB P	ROCESS ALLEL	SORS
0PEF	0	1	2	3	4
	m	h	d	b	а
AT	n	k	g	d	b
NO No	0	l	h	е	С
В		m	j	g	д
N		n	k	h	е
S		0	ì	i	f
2			m	j	g
980			n	k	h
贸			0	_	i
유				m	j
굕				n	k
OPERATION POINTS IN ORDER OF PERFORMANCE				0	1
					m
					n
m					0

FIG.5

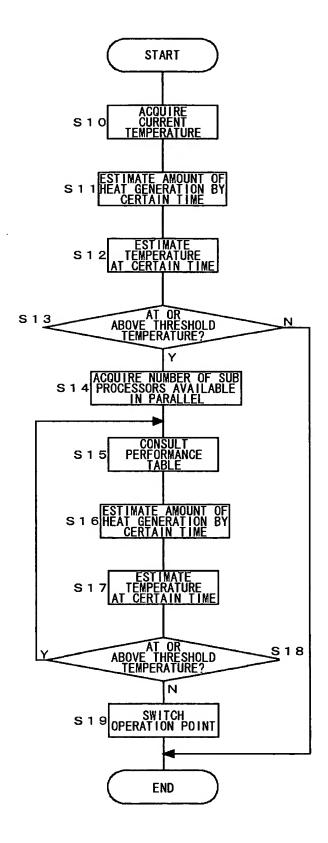


FIG.6

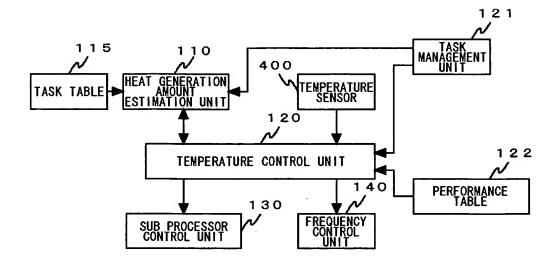


FIG.7

NUMBER OF SUB PROCESSORS AVAILABLE IN PARALLEL	AMOUNT OF HEAT GENERATION				
n 1	E1				
n 2	E 2				
n 3	E 3				
•					
•					
	n 2				

115

FIG.8

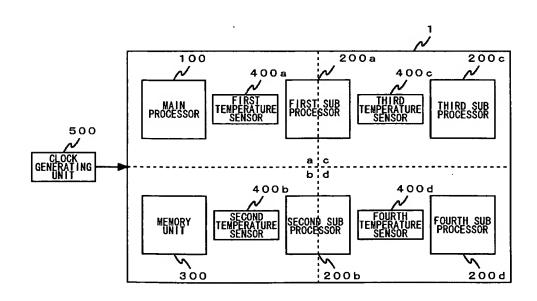
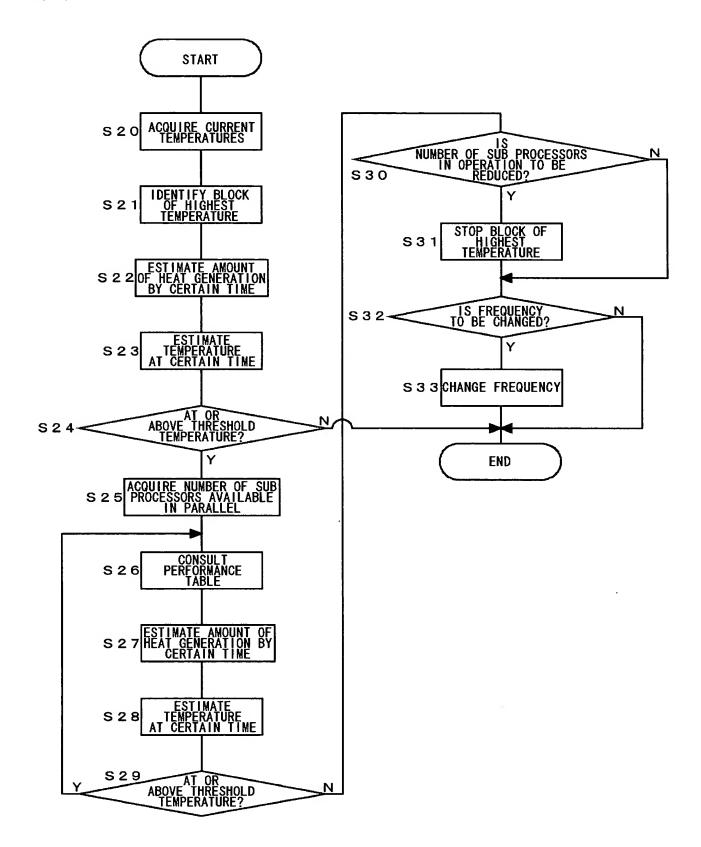


FIG.9



**FIG.10** 

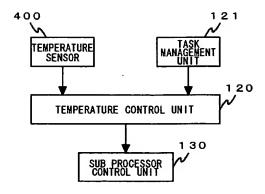


FIG.11

	TEMPERATURE OF SUB PROCESSOR
1	FOURTH SUB Processor
2	SECOND SUB Processor
3	THIRD SUB Processor
4	FIRST SUB PROCESSOR

FIG.12

